

Amendments to the Specification:

Please amend the paragraph on page 33, lines 3-17, as follows:

Message data **700** contains data segment 1 **702**, data segment 2 **704**, and data segment 3 **706**, which are similar to the data segments illustrated in **Figure 4**. In this example, these data segments form a packet **708**, which is placed into packet payload **710** within data packet **712**. Additionally, data packet **712** contains CRC **714**, which is used for error checking. Additionally, routing header **716** and transport header **718** are present in data packet **712**. Routing header **716** is used to identify source and destination ports for data packet **712**. Transport header **718** in this example specifies the destination queue pair for data packet **712**. Additionally, transport header **718** also provides information such as the operation code, packet sequence number, and partition for data packet **712**.

Please amend the paragraph that starts on page 33, line 27, and ends on page 34, line 9, as follows:

In **Figure 8**, a portion of a distributed computer system is depicted to illustrate an example request and acknowledgment transaction. The distributed computer system **800** in **Figure 8** includes a host processor node **802** and a host processor node **804**. Host processor node **802** includes a host channel adapter **806**. Host processor node **804** includes a host channel adapter **808**. The distributed computer system in **Figure 8** includes a SAN fabric **810**, which includes a switch **812** and a switch **814**. The SAN fabric includes a link coupling host channel adapter **806** to switch **812**; a link coupling switch **812** to switch **814**; and a link coupling host channel adapter **808** to switch **814**.

Please amend the paragraph on page 34, lines 10-20, as follows:

In the example transactions, host processor node **802** includes a client process A **816**. Host processor node **804** includes a client process B **818**. Client process A **816** interacts with host channel adapter hardware **806** through queue pair **820**. Client process B **818** interacts with hardware channel adapter hardware **808** through queue pair **822**. Queue pairs **824** and **828** are data structures that each include a send work queue and a receive work queue. Thus, queue pair

820 includes send work queue **824** and receive work queue **826**. Queue pair **828** includes send work queue **828** and receive work queue **830**.